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This introduction to the organization and programming of the 8086 family of microprocessors used in IBM microcomputers and compatibles is comprehensive and thorough. Includes coverage of I/O control, video/graphics control, text display, and OS/2. Strong pedagogy with numerous sample programs illustrates practical examples of structured programming.

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This easy to read textbook provides an introduction to computer architecture, while focusing on the essential aspects of hardware that programmers need to know. The topics are explained from a programmer's point of view, and the text emphasizes consequences for programmers. Divided in five parts, the book covers the basics of digital logic, gates, and data paths, as well as the three primary aspects of architecture: processors, memories, and I/O systems. The book also covers advanced topics of parallelism, pipelining, power and energy, and performance. A hands-on lab is also included.

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The second edition contains three new chapters as well as changes and updates throughout.

This piece covers computer architecture at the instruction set architecture (ISA) and system design levels. Starting with foundation material on data representation and computer arithmetic, the book moves through the basic components of a computer architecture, covering topics at increasing levels of complexity up through CISC, network architecture, and parallel architecture. The authors have adopted the use of a SPARC-



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subset for an instructional ISA called "ARC" (A RISC Computer), which is carried through the mainstream of the book, and is complemented with platform-independent software tools that simulate the ARC ISA as well as the MIPS and x86 (Pentium) ISAs.

FEATURES/BENEFITS Choice of the instruction set architecture (ISA). The mainstream ISA "ARC" is a subset of the commercial SPARC, which strikes a balance between the complexity of a real-world architecture and the need for a simple instructional ISA.

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available on Companion Website. Assembles and simulates program execution on SPARC-subset (ARC), MIPS, and Intel ISAs. Simulators and assemblers run on PCs, Macs, and Unix. Over 400 Adobe Acrobat slides Simplify lecture preparation. Password-protected area of Companion Website. Case studies. Over 200 homework problems. The major portion of the text deals with a high level look at computer architecture, while the appendices and case studies cover lower level, technology-dependent aspects. Allows computer architecture to be studied at all levels.

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This comprehensive book provides an up-to-date guide to programming the Intel 8086 family of microprocessors, emphasizing the close relationship between microprocessor architecture and the implementation of high-level languages.

This widely used, fully updated assembly language book provides basic information for the beginning programmer interested in computer architecture, operating systems, hardware manipulation, and compiler

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writing. Uses the Intel IA-32 processor family as its base, showing how to program for Windows and DOS. Is written in a clear and straightforward manner for high readability. Includes a companion CD-ROM with all sample programs, and Microsoft® Macro Assembler Version 8, along with an extensive companion Website maintained by the author. Covers machine architecture, processor architecture, assembly language fundamentals, data transfer, addressing and arithmetic, procedures, conditional processing, integer arithmetic, strings and arrays, structures and macros, 32-bit Windows programming,

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language interface, disk fundamentals, BIOS-level programming, MS-DOS programming, floating-point programming, and IA-32 instruction encoding. For embedded systems programmers and engineers, communication specialists, game programmers, and graphics programmers.

This is the eBook of the printed book and may not include any media, website access codes, or print supplements that may come packaged with the bound book. Today's incoming students are more likely to be exposed to Java than ever before. Focusing on a modern

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architecture (the Java Virtual Machine, or JVM), this text provides a thorough treatment of the principles of computer organization in the context of today's portable computer. Students are given simple but realistic examples to gain a complete understanding of how computation works on such a machine. Juola makes the material useful and relevant in a course that is often difficult for second-year CS students.

Assembly Language for x86 Processors, 6/e is ideal for undergraduate courses in assembly language programming and introductory courses

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in computer systems and computer architecture. Written specifically for the Intel/Windows/DOS platform, this complete and fully updated study of assembly language teaches students to write and debug programs at the machine level. Based on the Intel processor family, the text simplifies and demystifies concepts that students need to grasp before they can go on to more advanced computer architecture and operating systems courses. Students put theory into practice through writing software at the machine level, creating a memorable experience that gives them the confidence to work in any

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OS/machine-oriented environment. Proficiency in one other programming language, preferably Java, C, or C++, is recommended.

This textbook introduces readers to assembly and its role in computer programming and design. The author concentrates on covering the 8086 family of processors up to and including the Pentium. The focus is on providing students with a firm grasp of the main features of assembly programming, and how it can be used to improve a computer's performance. All of the main features are covered in depth: stacks, addressing modes,



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arithmetic, selection and iteration, as well as bit manipulation. Advanced topics include: string processing, macros, interrupts and input/output handling, and interfacing with such higher-level languages as C. The book is based on a successful course given by the author and includes numerous hands-on exercises.

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